

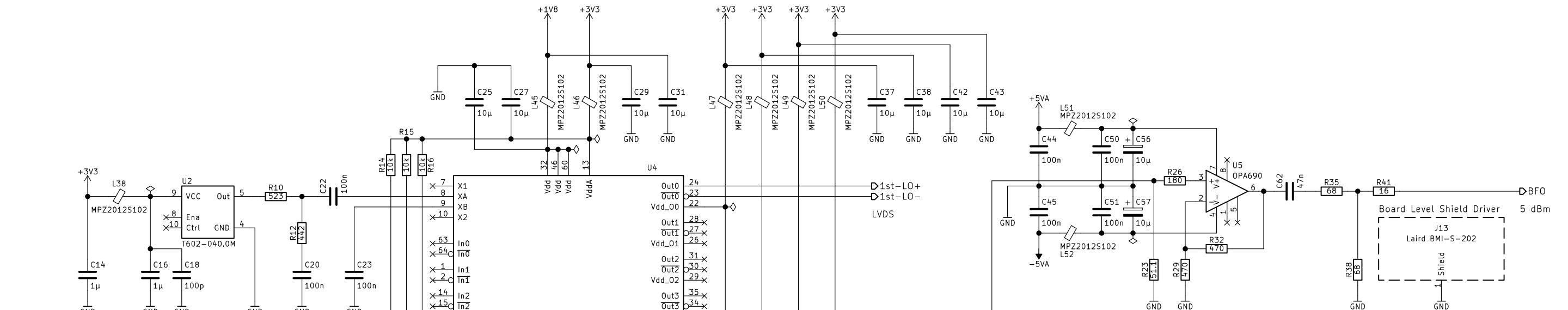
1 2 3 4 5 6 7 8

TCXO

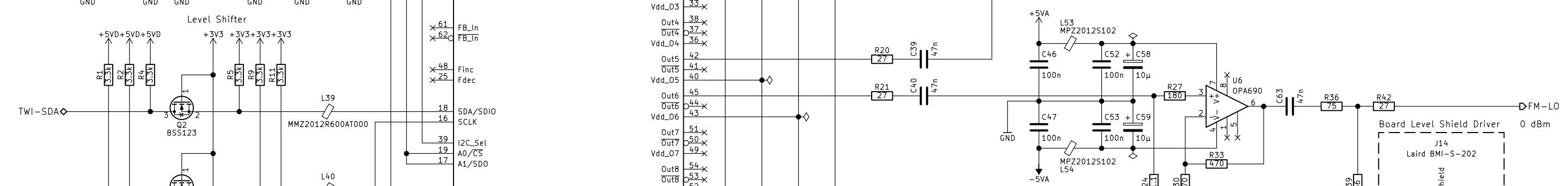
PLL Synthesizer

Line Driver

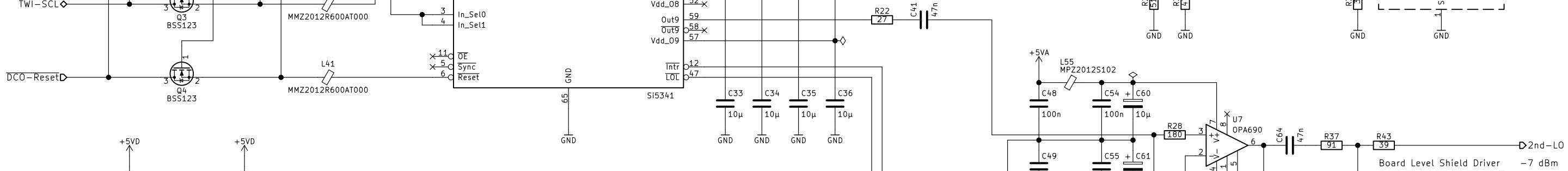
A



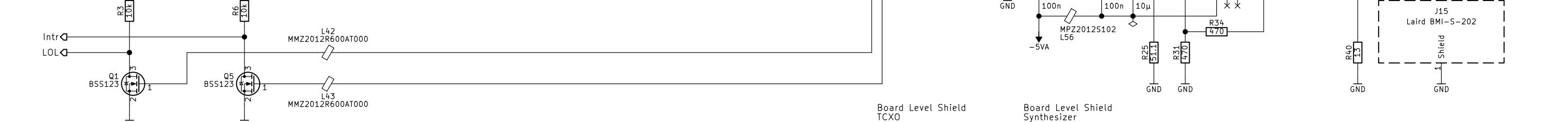
B



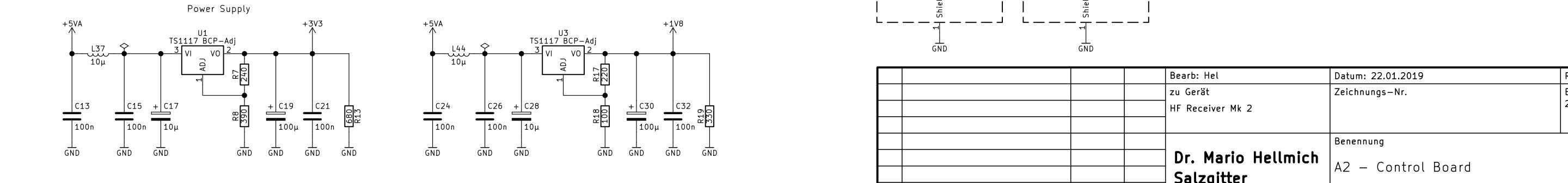
C



D



E



Rev	Änderung	Datum	Name
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**Dr. Mario Hellmich
Salzgitter**

Bearb: Hel	Datum: 22.01.2019	Rev: 0
zu Gerät	Zeichnungs-Nr.	
HF Receiver Mk 2		Blatt 2/3
Benennung		
A2 – Control Board		
Schaltplan-Seite: /Synthesizer/		

1 2 3 4 5 6 7 8

